

A Monopole-Probe-Based Quasi-Optical Amplifier Array

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Abstract—A quasi-optical amplifier array designed for *Ku*-band operation is presented. Orthogonal monopole-probe antennas are used to couple the power into, and out from, an array of microstrip-based amplifiers. The array has a gain of 5.4 dB at 16.4 GHz using packaged high electron mobility transistors (HEMT's). This gain is 3.5 dB below the maximum stable gain for this device at this frequency.

Index Terms—Active antennas, active arrays, FDTD methods, Gaussian beams, lens-focused measurements, microstrip, millimeter-wave amplifiers, millimeter-wave power dividers/combiners, probe antennas, Quasi-optical amplifiers.

I. INTRODUCTION

A NUMBER of different quasi-optical amplification schemes have been reported for combining the output of large numbers of solid-state amplifiers. These approaches primarily differ in the coupling method used to feed the ensemble of amplifiers. Among the proposed quasi-optical amplifier arrays are grid arrays [1]–[4], slot-based arrays [5]–[7], patch-based arrays [8]–[12], monopole-probe-based arrays [13]–[16], and slab combiners [17], [18].

Grid approaches feature a compact cell size. The proposed amplifier arrays based on slots and patches [5]–[12] need to be significantly larger than the grid arrays to produce the same amount of output power. However, for slot and patch designs, the amplification takes place in a standard transmission-line environment, thereby allowing the use of established coplanar waveguide (CPW) or microstrip multistage amplifier designs.

The monopole-probe-based approach seeks to combine the positive attributes of grid designs and slot/patch designs. Like the grid approach, the probe-based design has two output devices per cell and a high density of devices. Furthermore, the probe approach is amenable to CPW or microstrip-amplifier integration.

II. DESIGN

The monopole-probe-based amplifier array cell is pictured in Fig. 1. The array design starts with the testing of a single unit cell in a 4-GHz square-waveguide test set [13]. In this step, a number of passive unit cells (unit cells with the transistors

replaced with straight through microstrip transmission lines) were tested, and then amplifiers were inserted into the most promising passive cells. The cell with the lowest insertion loss was then scaled to *Ka*-band and duplicated to form the passive array described in [14]. This passive array is similar to the array of Fig. 1, but with the amplifiers replaced by transmission-line feed-throughs. The performance of a passive array gives an indication of how well the incident beam is coupled into, and out from, the input and output antennas. A well-designed array will serve to rotate the polarization of the incident signal with minimal insertion loss. As described in [14], the *Ka*-band passive array rotates the polarization of an incident beam with only 1 dB of insertion loss at 34 GHz and a fractional 3-dB bandwidth of 18%. This array was accurately modeled using a three-dimensional finite-difference time-domain (3-D FDTD) method, as reported in [19].

As shown in Fig. 1, the monopole-probe-based array has an inductive mesh (a square metal grid) on one side of a substrate, which serves as the ground plane for the microstrip lines patterned on the opposite side. Meshes of this type have the frequency characteristic of a low *Q* bandpass filter [20]–[23], with the transmission band occurring at frequencies just below the Rayleigh frequency of $f_R = c/ng$, where c is the phase velocity of the incident beam, n is the index of refraction of the substrate, and g is the mesh period. A normally incident beam at a frequency within the mesh's bandpass region will excite fields in the mesh apertures, which are predominantly (approaching 100%) the TE₁₀ square-waveguide mode. Thus, by choosing the grid period appropriately, the performance of the cells in the planar design of Fig. 1 will be similar to the square-waveguide performance of [13].

As vertically polarized incident waves strike the mesh, the electric fields which are excited in the mesh apertures are coupled into 50-Ω microstrip transmission lines through the vertically oriented monopole-probe antennas. The signal is then amplified, rotated in polarization, and finally re-transmitted in the orthogonal polarization. The array is designed to be placed between two orthogonal polarizers [Fig. 1(a)]. The polarizers help to maximize the coupling into, and out from, the antennas, and, in addition, ensure that the input and output beams are cross-polarized, which helps to minimize unwanted feedback. Incident power that is not coupled into the vertical *E*-field probe antennas will be reflected back into the input by the output polarizer. The degree of coupling into the probe antennas is dependent on the spacing l_1 and l_2 of the meshes away from the array, and the dimensions of the probe antenna [13], [24].

Manuscript received September 20, 1996; revised April 25, 1997. This work was supported by the Army Research Office under Contract DAAH04-94-G-0087.

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Publisher Item Identifier S 0018-9480(97)05375-1.

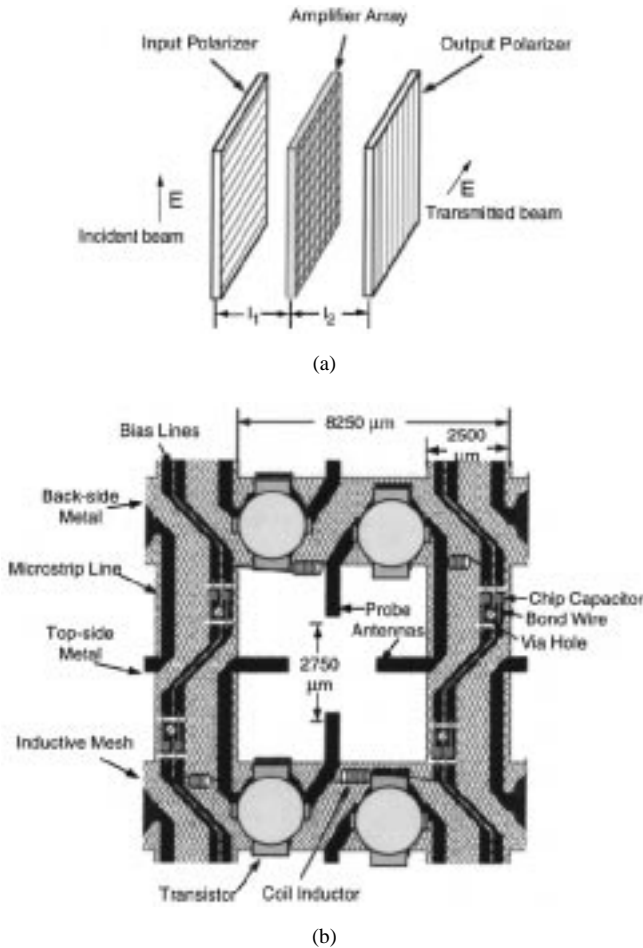


Fig. 1. (a) Probe amplifier array shown, placed between two strip-grating polarizers and (b) optimized monopole-probe-based unit cell showing the positions of the transistors and the biasing networks.

Probe antennas were used because their smaller footprint permits two sets of input and output antennas per cell. This allows for two microstrip amplifier circuits per cell, which effectively doubles the output-power per-unit-array area. Probe antennas also have an advantage over slot/patch designs in terms of loss and efficiency. As demonstrated by waveguide-to-microstrip transitions and waveguide-to-CPW transitions [25]–[27], probe antennas (with an offset backshort) have low loss over a broad millimeter-wave bandwidth when coupling to an incoming beam at normal incidence.

Whereas the Ka -band design in [14] is well suited for monolithic fabrication, it is too small to build in hybrid form. For this reason, the Ka -band passive array was scaled to Ku -band (where fabrication is easier), and packaged Fujitsu high electron mobility transistors (HEMT's) were inserted into the microstrip paths to produce a Ku -band active array. Using the FDTD modeling approach of [19], a passive array similar to [14], but scaled for X -band operation, was first designed and fabricated with its measured performance shown in Fig. 2. This passive X -band array, which was modified to form the active Ku -band array, exhibits good agreement with theory and has a peak insertion loss of less than 1 dB and a fractional 3-dB bandwidth of 27.5%. As expected, the reflected power drops down in the frequency region of

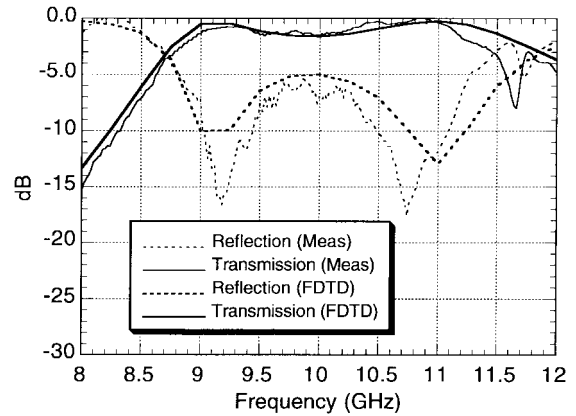


Fig. 2. X -band performance of scaled passive array compared with FDTD theory for input and output polarizer spacings of 2 and 4 mm, respectively.

good transmission. The array was tested using the two-port focused-Gaussian-beam method described in [14], [15]. The array was built on 20-mil-thick Rogers TMM-10 substrate with dielectric constant $\epsilon_r = 9.8$, and 1/2-oz (17- μm) copper metallization. The high dielectric-constant substrate material was chosen to keep the design compatible with eventual monolithic fabrication. This passive array's inductive mesh has a grid period, g , of 7750 μm and a grid linewidth, $2a$, of 2000 μm . The monopole-probe antennas are 2160- μm -long and 480- μm -wide.

This passive X -band array was then modified to accommodate the Fujitsu FHX35-LG HEMT's, as shown in Fig. 1. In this modified design, the grid period and linewidth was 8.25 and 2.5 mm, respectively. To reduce feedback between the input and output antennas, the antenna spacing was increased by reducing the antenna length to 1.5 mm. To minimize feedback through the bias lines, the lines are curved and each bias connection is made through a bias-tee network. Each transistor's gate and drain leads are connected to the bias line through an RF-choke inductor. The dc side of the choke inductor is connected to a 43-pF chip capacitor, which acts as an RF-ground. 18-nH choke inductors were used for the gate bias, while for the drain bias, 93-nH inductors were used. These values were arrived at semi-empirically to avoid bias oscillations. The inductors were 18-mil-diameter copper coils. The via holes for the transistors source leads were laser drilled using a Florod Laser Machine. The laser was also used to drill the holes for the vias in the bias lines and to trim the monopole-probe antennas to the optimal length.

III. PERFORMANCE

Because of the shorter probe antennas, the active array is expected to have peak performance at slightly higher frequencies than the X -band passive array. This was verified by testing a passive version of the array of Fig. 1 at both Ku - (12–18 GHz) and X -bands (8–12 GHz). By adjusting the polarizer positions, good rotation could be obtained at frequencies from 10–18 GHz. This agrees well with FDTD simulations, which were done for an array of these dimensions.

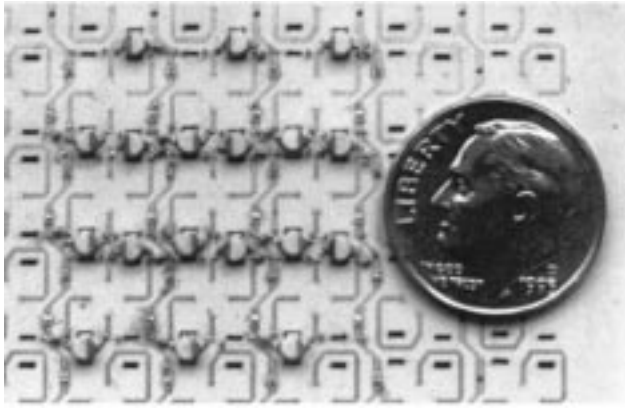


Fig. 3. Photograph of the completed active amplifier array.

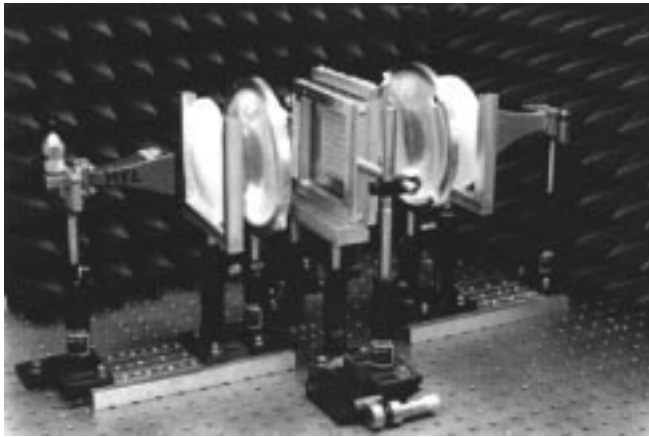


Fig. 4. Photograph of the Ku -band focused-Gaussian-beam test setup.

To test the active array, a 3×3 subsection of the array was populated with 18 transistors (2 per cell) and bias wires were attached. The fabricated array is shown in Fig. 3. Ferrite beads were added to the bias wires to avoid low-frequency (MHz) bias oscillations. If these oscillations are not suppressed, the devices, which have considerable low frequency gain, break down. The devices were then gradually biased and the polarizers adjusted to achieve maximum gain. The array measurements were performed with a Ku -band focused-Gaussian-beam test setup (Fig. 4) with the beam-waist apertured to the diameter of the 3×3 array subsection. The aperturing of the beam limits any contribution from the nonpopulated passive array cells. A broad-band antenna connected to a spectrum analyzer was placed just above the array to confirm the absence of oscillations.

A maximum gain of 5.4 dB with a 3-dB bandwidth of 2.4% was obtained at 16.4 GHz, as shown in Fig. 5. This measured gain is 3.9 dB less than the maximum available gain (MAG) of a conjugately matched device. The devices in the array have no input/output matching circuitry, although adjusting the polarizer positions tunes the input/output reactance, which the devices see. The data in Fig. 5 was obtained with the input and output polarizers spaced from the array surfaces at 9 and 7.5 mm, respectively. The bias conditions were $V_{gs} = -0.1$ V, $V_{ds} = 0.5$ V, and total $I_{ds} = 165$ mA. This total drain current corresponds to an average drain current of 9.2 mA

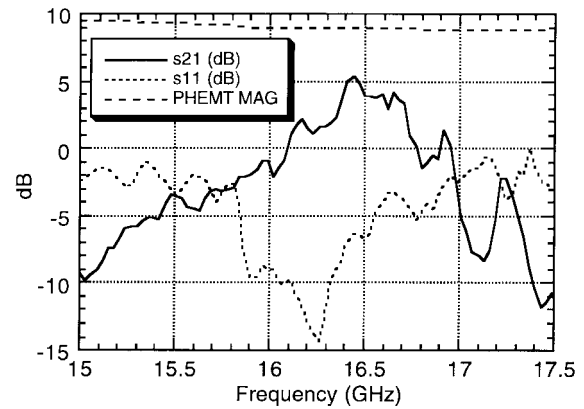


Fig. 5. Measured gain (s_{21}) and reflection loss (s_{11}) for amplifier array with input and output polarizers spaced from the array surfaces at 9 and 7.5 mm, respectively. For comparison, the HEMT's MAG is also plotted.

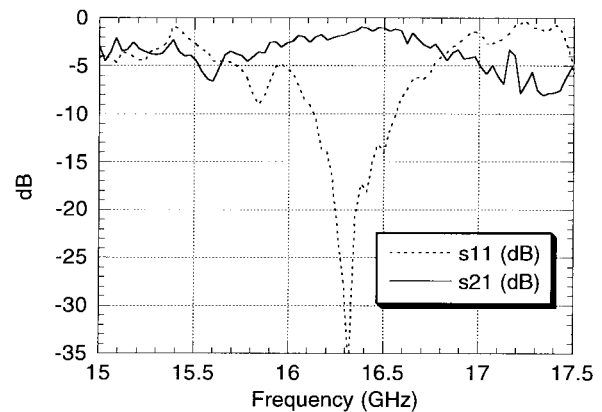


Fig. 6. Measured transmission (s_{21}) and reflection (s_{11}) for passive array with input and output polarizers spaced from the array surfaces at 9 and 7.5 mm, respectively.

per device. No oscillations were observed. As a further check for oscillations, this gain was measured at different RF-power levels, thereby, verifying that the output power varied linearly with input power.

However, when the drain voltage is increased over 0.5 V, oscillations begin to appear at 8.9 and 14.1 GHz. These oscillations indicate the presence crosstalk and feedback between adjacent amplifier elements. It should be noted that when a single transistor in an array cell is biased alone, no oscillations are observed—even at high bias levels. To operate at the higher bias levels where the gain is higher and oscillations occur, the adjacent elements must be spaced further apart, or alternatively, resistors which effectively reduce the transistors' gain could be placed in the transistor gate and/or drain paths, as done in the grid arrays [1]–[4]. However, because the addition of resistors reduces device gain, this approach may not yield additional total gain.

Fig. 6 shows the passive-array performance of this design with input and output polarizers at the same positions as in Fig. 5. The frequencies of maximum gain between 16.1 and 16.7 GHz for the active array correspond to dips in the reflection coefficient s_{11} , for both the active and passive arrays, and correspondingly good transmission for the passive array.

IV. CONCLUSION

A Ku -band active-amplifier array based on the passive microstrip Ka -band array of [14] has been described. A passive array was first designed and tested. This scaled passive array exhibited excellent performance with an insertion loss of less than 1 dB and a 3-dB bandwidth of 27.5%. These results agree well with FDTD simulations. Transistors were then added to the passive array to obtain 5.4 dB of gain at 16.4 GHz.

ACKNOWLEDGMENT

The authors would like to thank Rogers Corporation for donating the TMM-10 substrates and Microwave Components, Inc., for donating the inductors used in this work.

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